

BLAKELY SOKOLOFF TAYLOR & ZAFMAN LLP

TELEPHONE: (408) 720-8300

INTELLECTUAL PROPERTY LAW
12400 WILSHIRE BOULEVARD, 7TH FLOOR
LOS ANGELES, CA 90025

FACSIMILE: (408) 720-8383

BEST AVAILABLE COPY

FACSIMILE COVER SHEET

RECEIVED
CENTRAL FAX CENTER

JAN 15 2004

OFFICIAL

Deliver to: Unassigned, USPTO Art Group: 2819
 Facsimile No.: 703-872-9318 Date: January 15, 2004
 From: Robert D. O'Rourke, Reg. No. 46,972

Our Docket No.: 2986IP035 Number of pages 5 including this sheet.Application No.: 09/724,585 Filing Date: 11/28/2000

Docket Due Date(s): _____

Enclosed are the following documents:

<input type="checkbox"/> Amendment: _____	<input type="checkbox"/> Issue Fee Transmittal
<input type="checkbox"/> Appeal Brief (In triplicate) (_____ pgs)	<input type="checkbox"/> Notice of Appeal
<input type="checkbox"/> Application: _____	<input type="checkbox"/> Petition for: _____
(_____ pgs) w/cover & abstract)	<input type="checkbox"/> Request for Continued Examination (RCE)
<input type="checkbox"/> Assignment & Cover Sheet (_____ pgs)	<input type="checkbox"/> Reply Brief (_____ pgs)
<input checked="" type="checkbox"/> Certificate of Facsimile _____	<input type="checkbox"/> Request & Certification Under 35 USC 122(b)(2)(B)(i)
<input type="checkbox"/> Continued Prosecution Application (CPA)	<input type="checkbox"/> Request to Rescind Previous Nonpublication Request
<input type="checkbox"/> Declaration & POA (_____ pgs)	<input type="checkbox"/> Response to Notice of Missing Parts & Formalities Letter
<input type="checkbox"/> Drawings: _____ sheets, _____ figures	<input type="checkbox"/> Response to Written Opinion (_____ pgs)
<input type="checkbox"/> Extension of Time: _____	<input type="checkbox"/> Terminal Disclaimer
<input type="checkbox"/> Fee Transmittal (in duplicate)	<input type="checkbox"/> Transmittal of Publication Fee Due
<input type="checkbox"/> IDS & PTO/SB/08 (_____ pgs)	<input type="checkbox"/> Transmittal Letter
<input checked="" type="checkbox"/> Other Facsimile Transmittal (1) page	
Cross-Reference Under 37 C.F.R. 1.28(a)(2), To Related Applications (4) pages	

CERTIFICATE OF MAILING/TRANSMISSION (37 CFR 1.8A)

I hereby certify that this correspondence is being transmitted by facsimile on the date shown below to the United States Patent and Trademark Office.

Janice Shannon
 Janice Shannon

1/15/2004
01/15/2004
 Date

Confidentiality Notice: The documents accompanying this facsimile transmission contain information from the law firm of Blakely, Sokoloff, Taylor & Zafman which is confidential or privileged. The information is intended to be for the use of the individual or entity named on this transmission sheet. If you are not the intended recipient, be aware that any disclosure, copying, distribution or use of the contents of this faxed information is prohibited. If you have received this facsimile in error, please notify us by telephone immediately so that we can arrange for the retrieval of the original documents at no cost to you.

If you do not receive all the pages, or if there is any difficulty in receiving, please call: (408) 720-8300 and ask for Janice Shannon.

Attorney's Docket No.: 002986, P035

PatentRECEIVED
CENTRAL FAX CENTER

JAN 15 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

John Mark Beardslee, et al.

Application No: 09/724,585

Filed: November 28, 2000

For: Method and System For Debugging
An Electronic System

Examiner: Unassigned

Art Unit: 2819

Confirmation No.: 7620

OFFICIAL

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450CROSS-REFERENCE UNDER 37 C.F.R. §1.78(a)(2) TO RELATED APPLICATIONS

Sir:

Pursuant to 37 C.F.R. §1.78(a)(2), applicants hereby cross-reference the following related patent applications, which is assigned to the same assignee as the present application:

CERTIFICATE OF TRANSMISSION

I hereby certify that this correspondence is being facsimile transmitted to the Patent and Trademark Office on:

Date of Transmission January 15, 2004Jancee Shannon

(Typed or printed name of person transmitting paper)

Jancee Shannon 1-15-2004

(Signature of person transmitting paper)

09/724,585

1

02986.P035

BEST AVAILABLE COPY

- ✓(1) U.S. Patent Application No. 09/724,702, filed on November 28, 2000, now U.S. Patent No. 6,581,191 B1, entitled, Hardware Debugging In A Hardware Description Language, by Nils Endric Schubert, John Mark Beardslee and Douglas L. Perry.
- ✓(2) U.S. Patent Application No. 10/406,732, filed on April 2, 2003, entitled, Hardware Debugging In A Hardware Description Language, by Nils Endric Schubert, John Mark Beardslee and Douglas L. Perry.
- ✓(3) U.S. Patent Application No. 10/377,907, filed on February 28, 2003, entitled, Hardware-based HDL Code Coverage and Design Analysis, by Nils Endric Schubert, John Mark Beardslee, Gernot Heinrich Koch and Ewald John Detjens.
- ✓(4) U.S. Patent Application No. 10/212,128, filed on July 31, 2002, entitled, Method and System For Debugging An Electric System Using Instrumentation Circuitry and A Logic Analyzer, by Nils Endric Schubert, John Mark Beardslee, Gernot Heinrich Koch and Olaf Poeppe.
- (5) U.S. Patent Application No. 10/456,768, filed on June 6, 2003, entitled, Hardware/Software Co-Debugging In A Hardware Description Language, by Nils Endric Schubert, Kenneth S. McElvain, John Mark Beardslee and Mario Larouche.

BEST AVAILABLE COPY

- ✓(6) U.S. Patent Application No.10/210,509, filed on July 31, 2002 ,
entitled, Method and User Interface For Debugging An Electronic
System, by Nils Endric Schubert, John Mark Beardslee, Gernot
Heinrich Koch and Olaf Poeppel.
- ✓(7) U.S. Patent Application No.09/724,839, filed on November 28, 2000 ,
entitled, Design Instrumentation Circuitry, by Nils Endric Schubert,
John Mark Beardslee and Douglas L. Perry.
- ✓(8) U.S. Patent Application No. 09/724,840, filed on November 28, 2000,
now U.S. Patent No. 6,618,839 B1, entitled, Method and System For
Providing An Electronic System Design With Enhanced Debugging
Capabilities, by John Mark Beardslee, Nils Endric Schubert, and
Douglas L. Perry.
- (9) U.S. Patent Application No.10/455,917, filed on June 6, 2003, entitled,
Enhanced Hardware Debugging With Embedded FPGAS In A
Hardware Description Language, by Nils Endric Schubert, Kenneth S.
McElvain, John Mark Beardslee and Mario Larouche.

BEST AVAILABLE COPY


Authorization is hereby given to charge our Deposit Account No. 02-2666 for any charges that may be due.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN

Date: _____

1/15/04



Robert B. O'Rourke
Reg. No. 46,972

12400 Wilshire Boulevard
Seventh Floor
Los Angeles, CA 90025-1026
(408) 720-8300

BEST AVAILABLE COPY